

# Li-Ion Power Gauge™ Module

#### **Features**

- ➤ Complete bq2050 Power Gauge solution for Li-Ion battery packs
- ➤ Battery information available over a single-wire bidirectional serial port
- ➤ Battery state-of-charge monitoring for 2- to 4-cell series applications
- On-board regulator allows direct connection to the battery
- ➤ "L" version includes push-button activated LEDs to display state-of-charge information
- > Nominal capacity pre-configured
- Compact size for battery pack integration

#### **General Description**

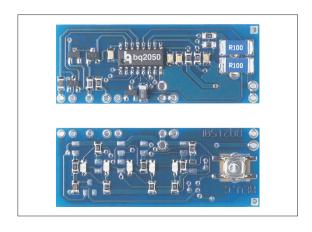
The bq2150 Power Gauge Module provides a complete and compact solution for capacity monitoring of Li-Ion battery packs. Designed for battery pack integration, the bq2150 incorporates a bq2050 Gas Gauge IC, a current sense resistor, and all other components necessary to accurately monitor and display the capacity of 2 to 4 series cells.

The bq2150L includes five LEDs to display remaining capacity in 20% increments of the learned capacity. The LEDs are activated with the onboard push-button switch.

Contacts are provided on the bq2150 for direct connection to the battery stack (BAT+, BAT-) and the serial communications port (DQ). The RBI input provides backup power to the bq2050 in the event that the cells are removed or the battery is turned off. The bq2150 has a 1µF capacitor onboard connected to RBI to supply backup power for about an hour. In battery packs that use high-side FETs to control the charge/discharge of the Li-Ion cells, the RBI input can be wired to a single cell to provide prolonged data retention times. The SD input allows an external signal (active low) to turn the bq2050 IC off to minimize internal current consumption of the battery pack and maximize storage life of the pack in the system. When turned off, the bq2050 is nonfunctional, and the RBI power source maintains register information. Please refer to the bq2050 data sheet for the specifics on the operation of the gas gauge.

Unitrode configures the bq2150 based on the information requested in Table 1. The configuration defines the number of series cells, the nominal battery pack capacity, and the Li-Ion battery type (coke or graphite anode). Figure 1 shows how the module connects to the cells.

5/99 B



A module development kit is also available for the bq2150. The bq2150B-KT or the bq2150LB-KT includes one configured module and the following:

- 1) An interface board that allows connection to the serial port of an AT-compatible computer.
- Menu-driven software to display charge/discharge activity and to allow user interface to the bq2050 from any standard DOS PC.
- 3) Source code for the TSR.

#### **Pin Descriptions**

P1	DQ/Serial Communications port
<b>P2</b>	No connect
P3	BAT+/Battery positive/pack positive
P4	SD/Shutdown
P5	RBI/Register backup input
P6	GND/Ground
<b>P7</b>	PACK-/Pack negative
<b>P</b> 8	BAT-/Battery negative

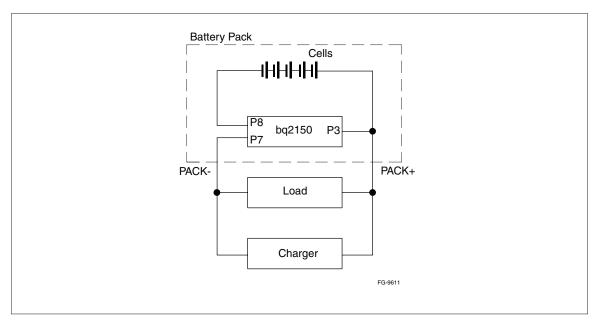
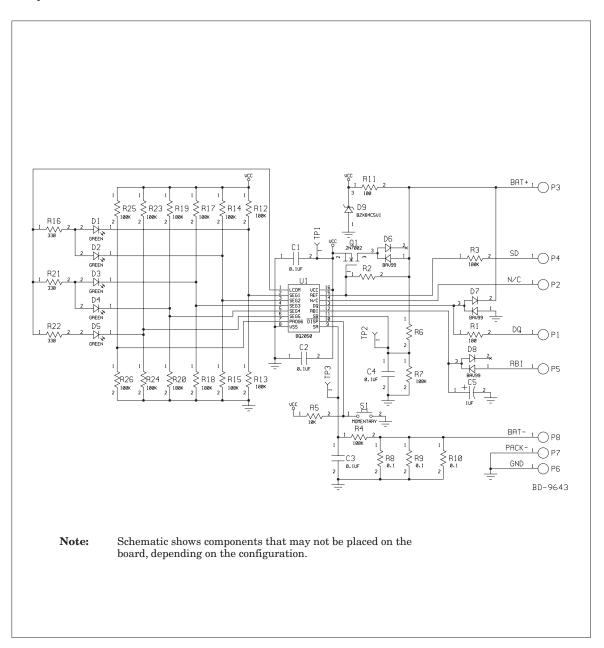


Figure 1. Module Connection Diagram

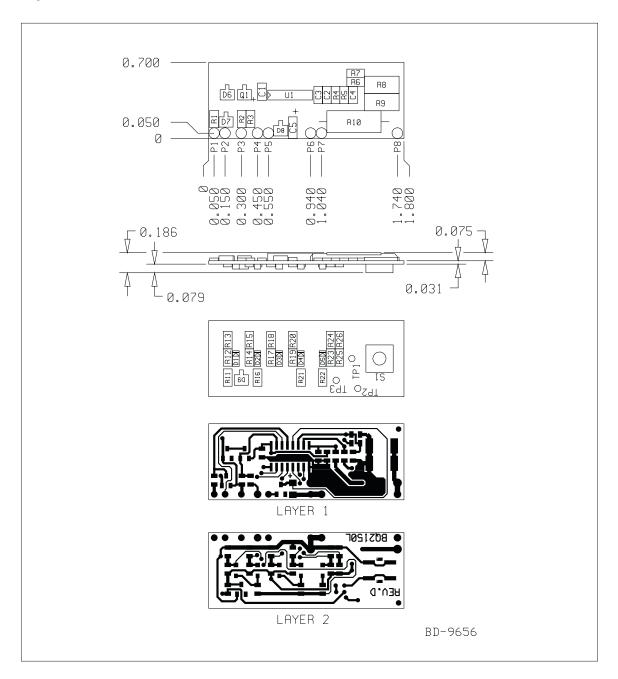
Table 1. bq2150 Module Configuration

Contact:			Phone:			
Address:						
Sales Contact:			Phone:			
Number of series battery cells (2-4)			_			
Coke or graphite cell anode			_			
Battery pack capacity (mAh)			_			
Discharge rate into load (3.0A max)	Min	Avg	Max			
Charge rate (3.0A max)			_			
Nominal Available Capacity after rese (Programmed Capacity or Zero)						
Self-discharge compensation (Y/N)			_			
LEDs and switch (Y/N)			_			

## bq2150 Schematic



## bq2150 Board



## **Absolute Maximum Ratings**

Symbol	Parameter	Minimum	Maximum	Unit	Conditions
$V_{\rm CC}$	Relative to VSS	-0.3	+7.0	V	bq2050
All other pins	Relative to VSS	-0.3	+7.0	V	bq2050
PSR	Continuous sense resistor power dissipation	-	3	W	Thru-hole sense resistor
		-	1	W	Surface-mount sense resistor
I <sub>CHG</sub>	Continuous charge/dis- charge current	-	3.0	A	
TOPR	Operating temperature	0	+70	°C	Commercial
TSTR	Storage temperature	-40	+85	°C	

Note:

Permanent device damage may occur if **Absolute Maximum Ratings** are exceeded. Functional operation should be limited to the Recommended DC Operating Conditions detailed in this data sheet. Exposure to conditions beyond the operational limits for extended periods of time may affect device reliability.

#### **DC Electrical Characteristics** (TA = TOPR)

Symbol	Parameter	Minimum	Typical	Maximum	Unit	Conditions/Notes
NumCell	Number of series cells in battery pack	2	-	4	-	
BAT+	Positive terminal of pack	GND NumCell * 3.6V N		NumCell * 5.4V	V	
BAT-	Negative terminal of pack	GND - 0.3	GND - 0.3 -		V	
ICC	Supply current at BAT <sub>1P</sub> terminal (no external loads)	- 200		300	μΑ	
$R_{\mathrm{DQ}}$	Internal pull-down	500k	-	-	$\Omega^1$	
I <sub>OL</sub>	Open-drain sink current DQ	-	-	5.0	mA <sup>1</sup>	
Vol	Open-drain output low, I <sub>OL</sub> DQ	-	-	0.5	V1	IOL < 5mA
V <sub>IHDQ</sub>	DQ input high	2.5	-	-	V1	
V <sub>ILDQ</sub>	DQ input low	-	-	0.8	V1	
Vos	Voltage offset	-	-	150	μV <sup>1</sup>	

Note:

1. Characterized on PCB, IC 100% tested.

#### DC Voltage Thresholds (TA = TOPR)

Symbol	Parameter	Minimum	Typical	Maximum	Unit	Notes
$V_{\mathrm{EDVF}}$	Final empty warning	1.45	1.47	1.49	V	BAT+/(2*NumCell) <sup>1</sup>
$V_{\rm EDV1}$	First empty warning	1.50	1.52	1.55	V	BAT+/(2*NumCell) <sup>1</sup>
$V_{ m MCV}$	Maximum single-cell voltage	2.20	2.25	2.30	V	BAT+/(2*NumCell) <sup>1</sup>
$V_{ m SRO}$	Sense range	-300	-	+2000	mV	$SR, V_{SR} + V_{OS}^2$
$V_{ m SRQ}$	Valid charge	210	-	-	μV	V <sub>SR</sub> + V <sub>OS</sub> <sup>2, 3</sup>
$V_{ m SRD}$	Valid discharge	-	-	-200	μV	$V_{SR} + V_{OS}$ 2, 3

Notes:

- 1. At SB input of bq2050
- 2. At SR input of bq2050.
- 3. Default value; value set in DMF register.

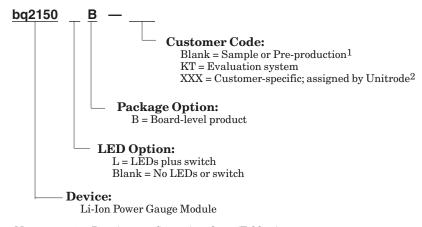
#### **Data Sheet Revision History**

Change No.	Page No.	Description			
1	2	Updated Table 1 to include 3.0A limit			
1	5	Added 3.0A maximum continuous charge/discharge current specification			

Note:

Change 1 = May 1999 B changes from April 1999.

## **Ordering Information**



 $\textbf{Notes:} \qquad 1. \quad \text{Requires configuration sheet (Table 1)}$ 

2. Example production part number: bq2150LB-001

#### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1999, Texas Instruments Incorporated