

LA2900M

Two-Channel High-Output Line Amplifier for Car Audio Systems

Overview

The LA2900M is a 2-channel high-output line amplifier IC developed for use in car audio systems. It can provide significant improvements in the signal-to-noise ratio when used to drive an external power amplifier due to its significantly increased output voltage as compared to that of earlier preamplifiers.

The high output of the LA2900M allows it to achieve a high signal-to-noise ratio and excellent fidelity in audio systems in which the main unit is connected to an external amplifier, and can improve the performance of the power amplifier in such systems.

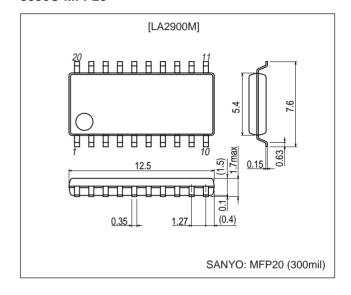
Functions and Features

- High output level (5.3 V rms)
- Low output noise voltage (12 μV)
- Low total harmonic distortion (0.003%)
- High ripple rejection ratio (70 dB)
- · Excellent audio fidelity

Package Dimensions

unit: mm

3036C-MFP20



Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	With no input signal	12	V
Allowable power dissipation	Pd max	Ta = 85°C, Mounted on a printed circuit board (114.3 \times 76.1 \times 1.6 mm ³ , glass epoxy)	400	mW
Operating temperature	Topr		-40 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

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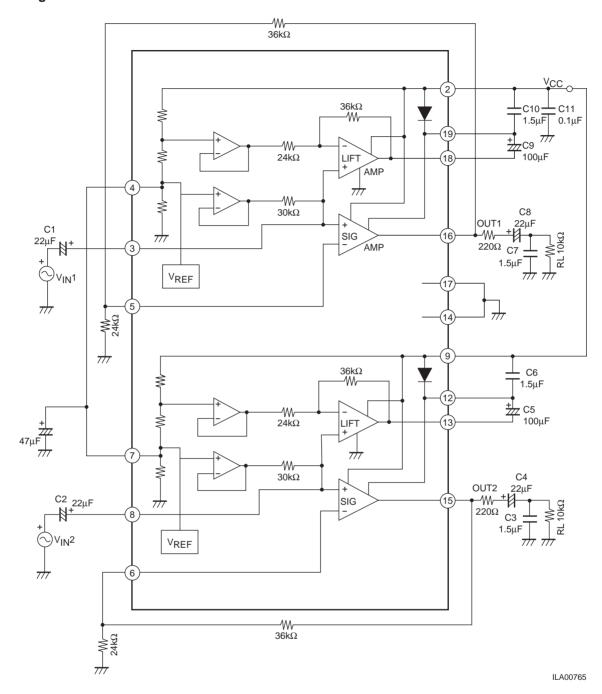
Recommended Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended operating voltage	V _{CC}		9	V
Allowable operating supply voltage range	V _{CCOP}		6 to 12	V
Recommended load resistance	RLOP		10	kΩ

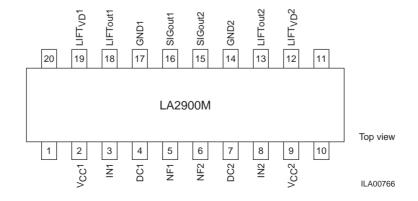
Electrical Characteristics at Ta = 25°C, V_{CC} = 9 V, RL = 10 k Ω , f = 1 kHz, Rg = 600 Ω

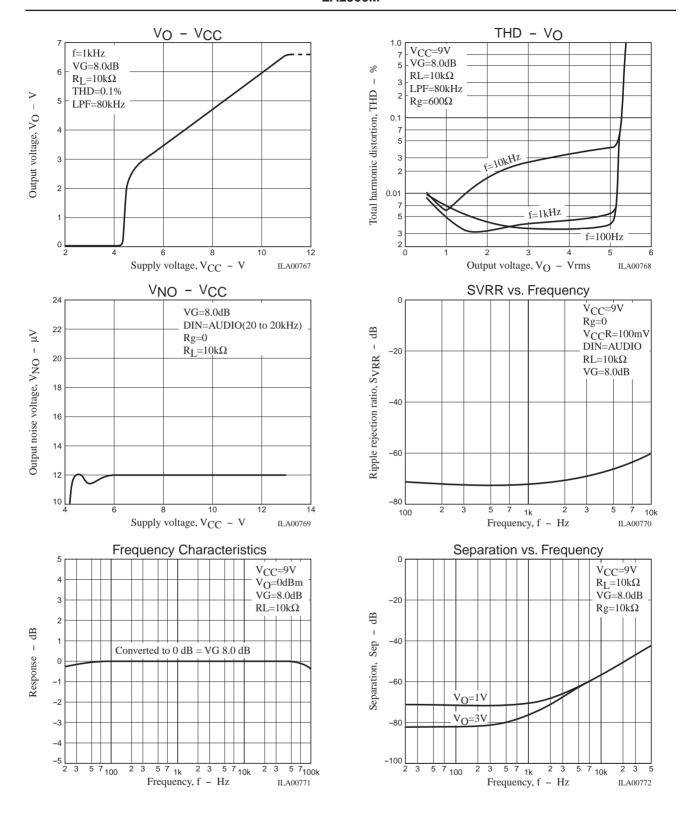
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Quiescent current	Icco	Rg = 0	7	11	15	mA
Voltage gain	V _G	$V_O = 0 \text{ dBm}$	7.5	8	8.5	dB
Output voltage	Vo	THD = 0.1%	5.0	5.3		Vrms
Total harmonic distortion	THD	V _O = 3 V, LPF = 80 kHz		0.003	0.01	%
Output noise voltage	V _{NO}	Rg = 0, BPF = 20 Hz to 20 kHz		12	17	μVrms
Ripple rejection ratio	SVRR	Rg = 0, f _R = 1 kHz, V _{CCR} = 100 mV, BPF = 20 Hz to 20 kHz	60	70		dB
Channel separation	CH _{sep}	$Rg = 10 \text{ k}\Omega, V_O = 1 \text{ Vrms}$	60	70		dB
Input resistance	Ri		20	30	39	kΩ

Block Diagram



Pin Assignment





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