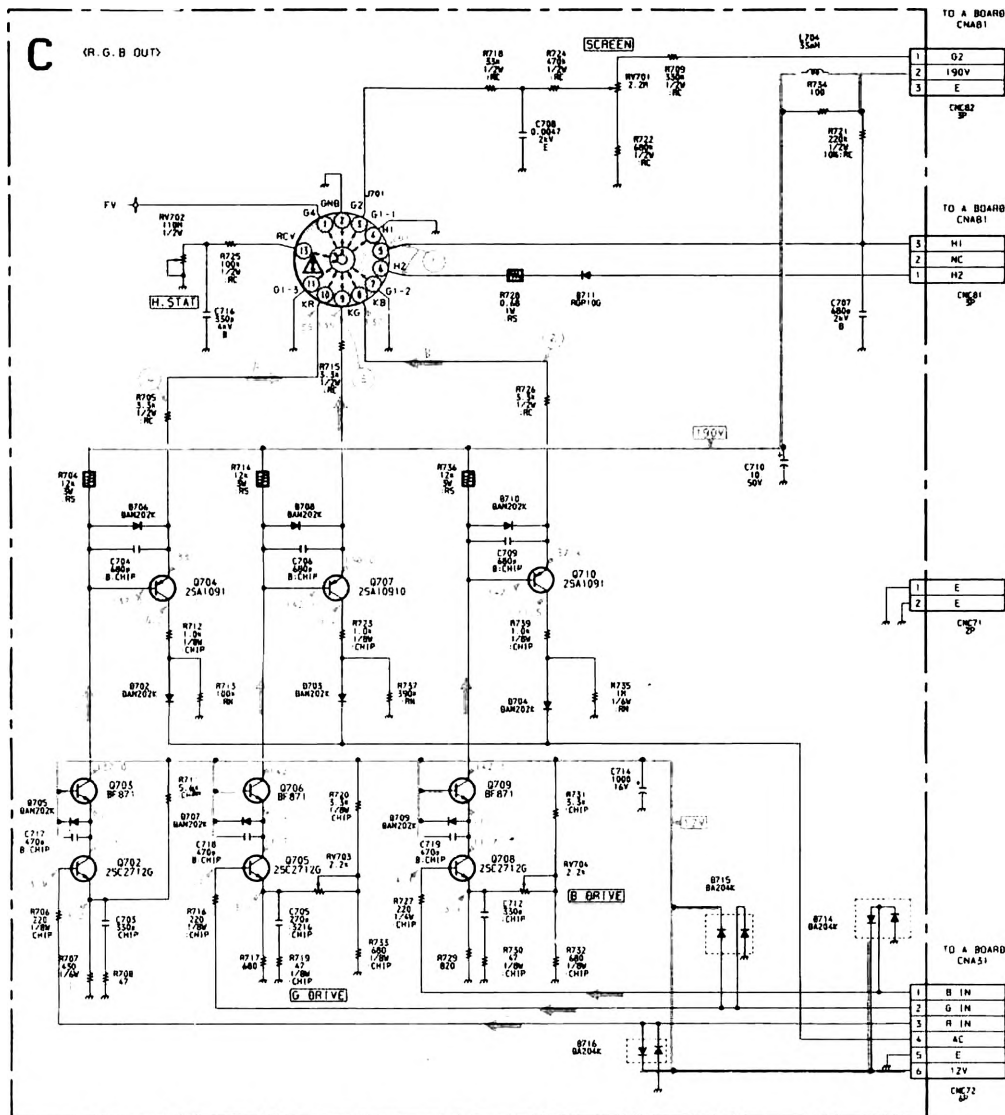
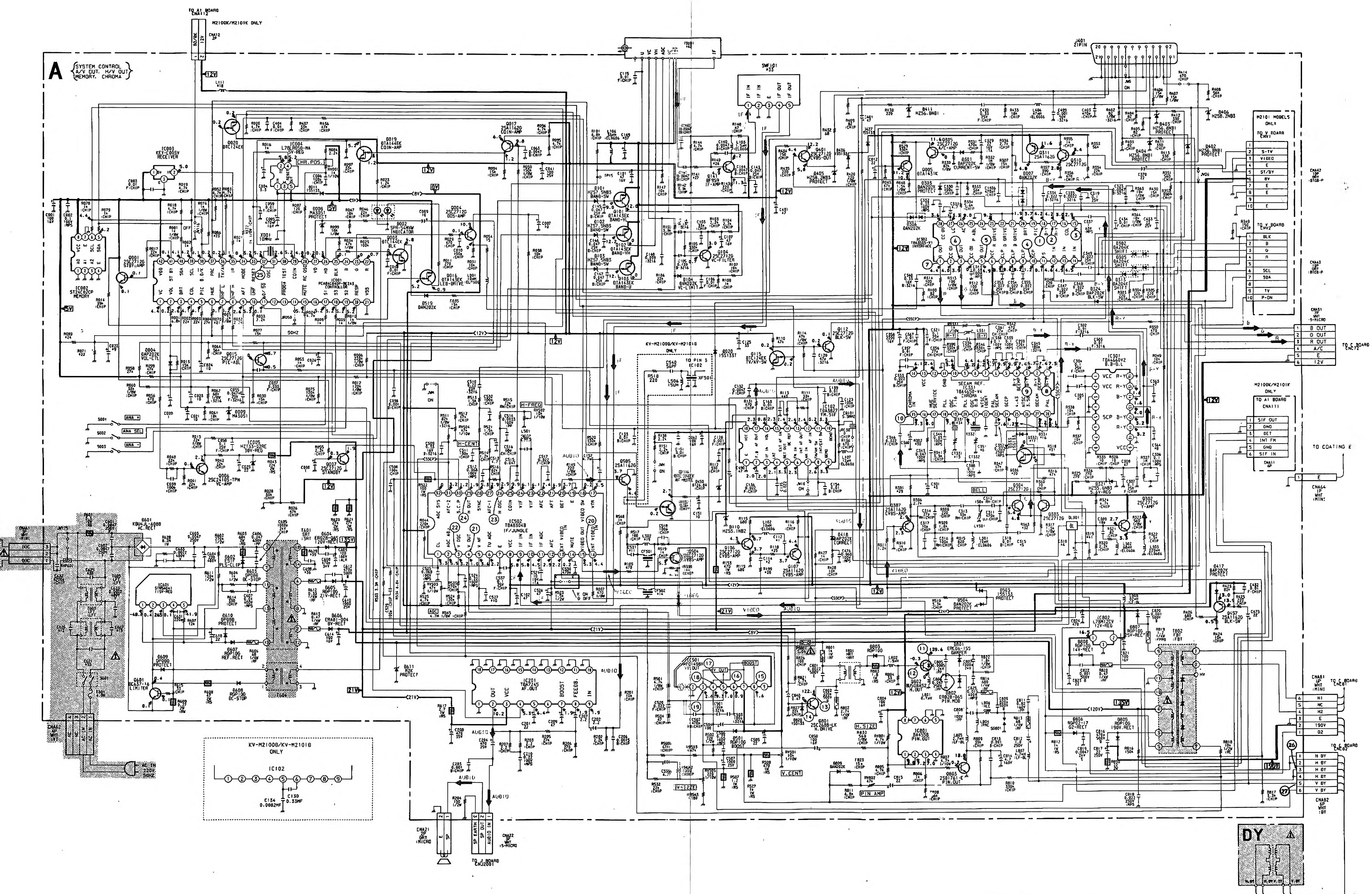




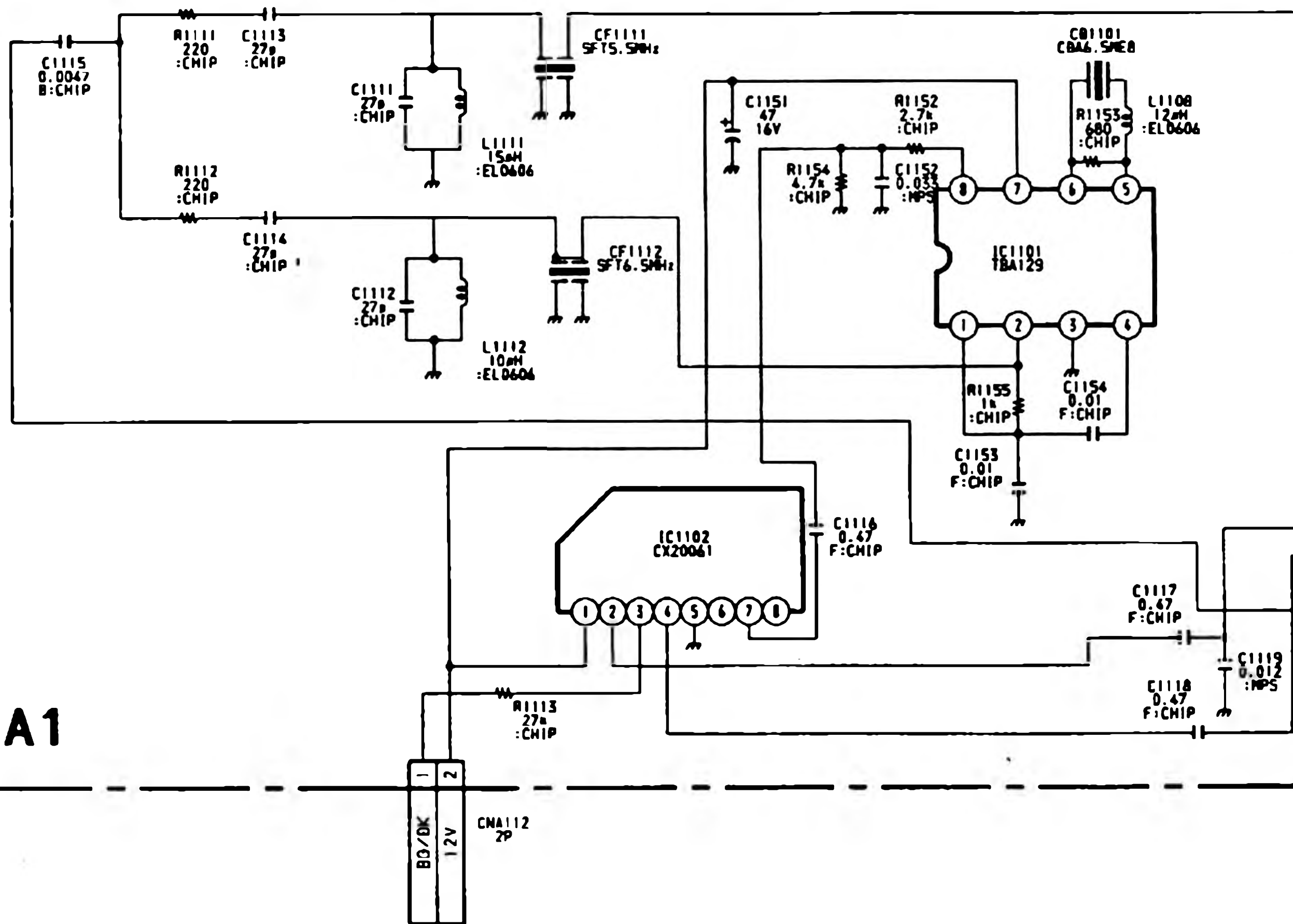
Rys. 3. Schemat elektryczny płytki kineskopu C





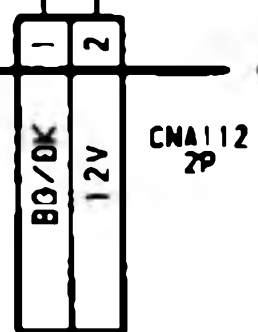


A1



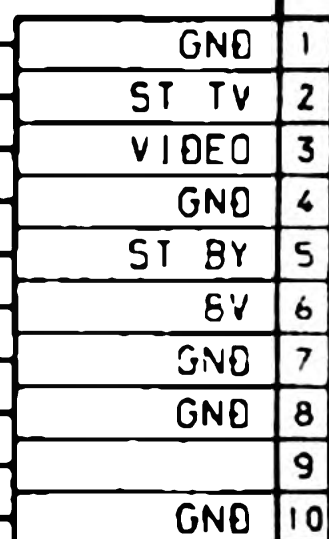
|   |         |
|---|---------|
| 1 | SIF OUT |
| 2 | GND     |
| 3 | DET     |
| 4 | INT FM  |
| 5 | GND     |
| 6 | SIF IN  |

CN111  
6P



O A BOARD  
CNA42

CNV1  
10P



TO A BOARD  
CNA43

CNV2  
10P

